

Substitute for form 1449B/PTO.

Complete if Known

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Application Number	09/905,067
Filing Date	July 16, 2001
First Named Inventor	Acharya
Group Art Unit	Not Assigned 266
Examiner Name	Wilson Not Assigned
Attorney Docket Number	95-512

of 1

OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RWN		DANIEL CASSIDAY, InfiniBand™ Architecture Tutorial, Hot Chips, August 2000, Sun Microsystems, 79 pages.	

Examiner Signature	Robert W. Wilson	Date Considered	7/6/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.